

tinyML[®] EMEA

Enabling Ultra-low Power Machine Learning at the Edge

tinyML EMEA Technical Forum 2021 Proceedings

June 7 – 10, 2021

Virtual Event



www.tinyML.org



tinyML EMEA Technical Forum 2021

June 7-10, 2021

Utilizing Static Code Generation in TinyML

Presented by: Rafael Stahl, Technical University of Munich, Germany

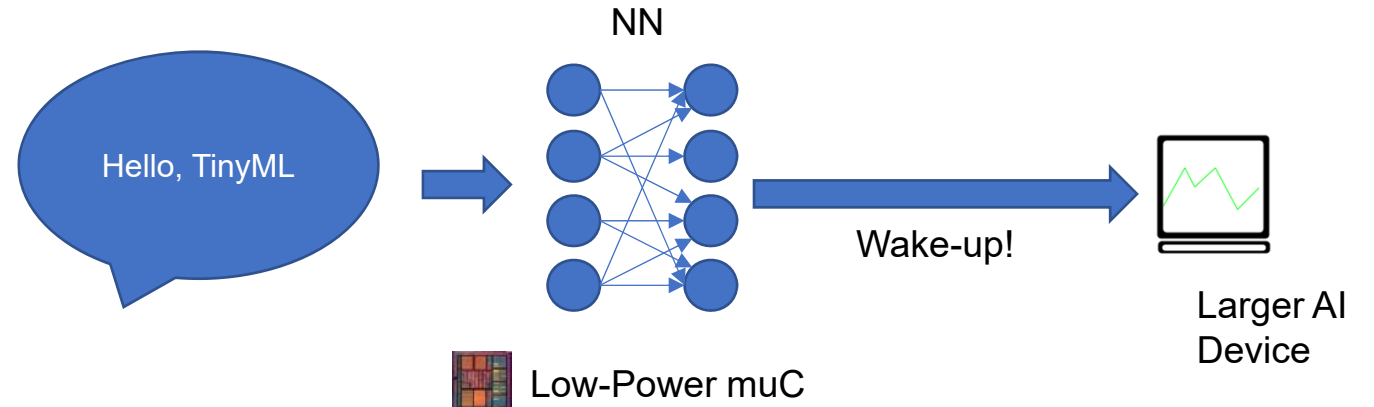
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- Machine Learning on Microcontrollers enables advanced, low power applications
- Constrained in terms of run time and memory usage
- Targeting overheads in TFLite Micro and μ TVM

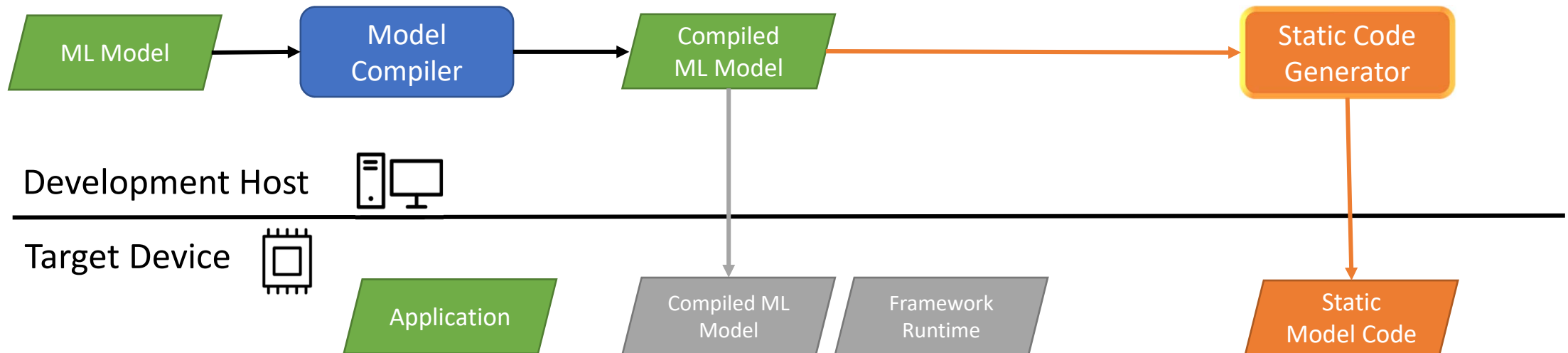




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- Runtime library on target → Generate static code
- Dynamic loading of graph and data → Only code and data for one particular model





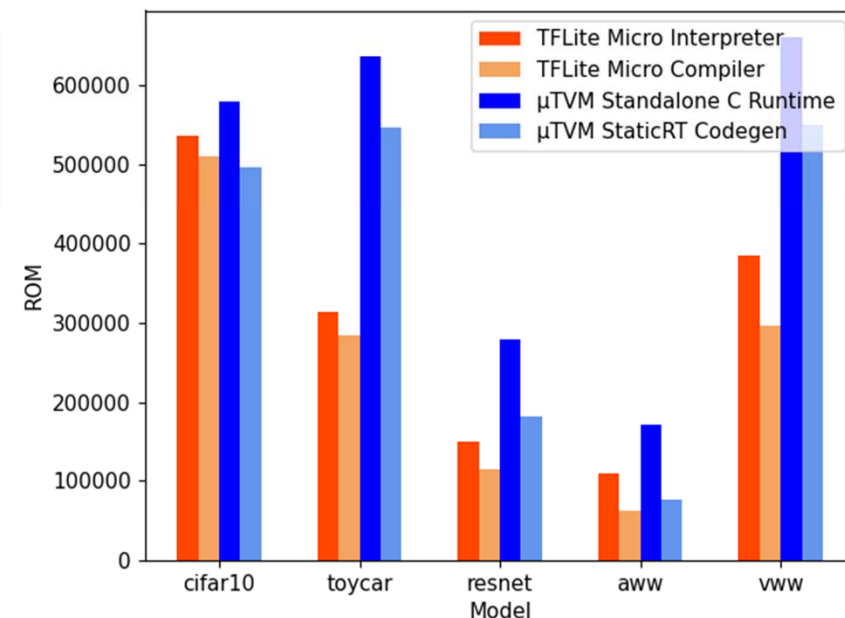
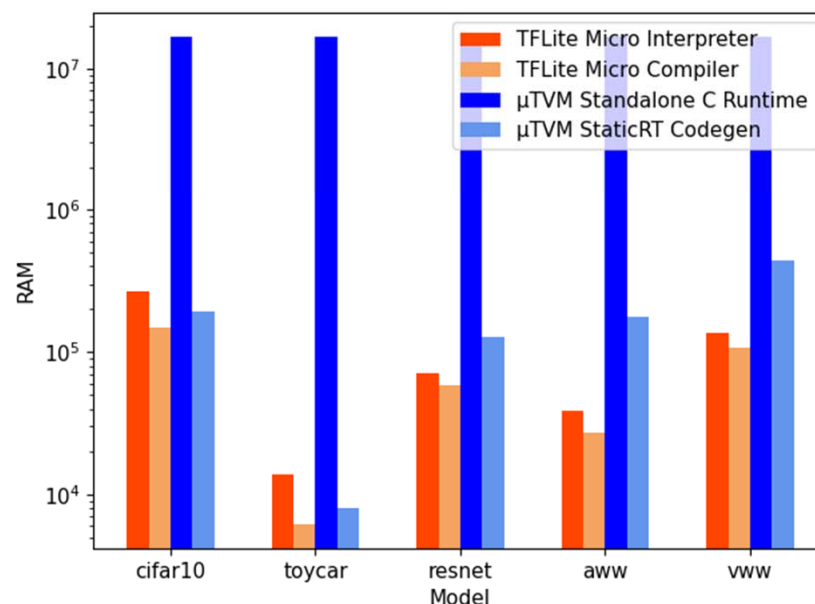
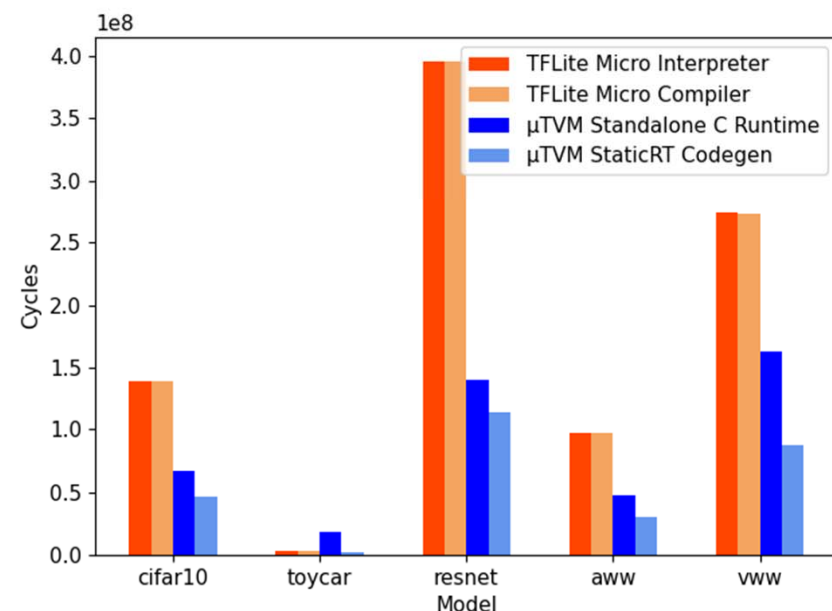
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- Evaluated using TinyMLPerf
- RISC-V on ETISS

TFLite Micro: https://github.com/tum-ei-eda/tflite_micro_compiler

μTVM: https://github.com/tum-ei-eda/utvm_staticrt_codegen



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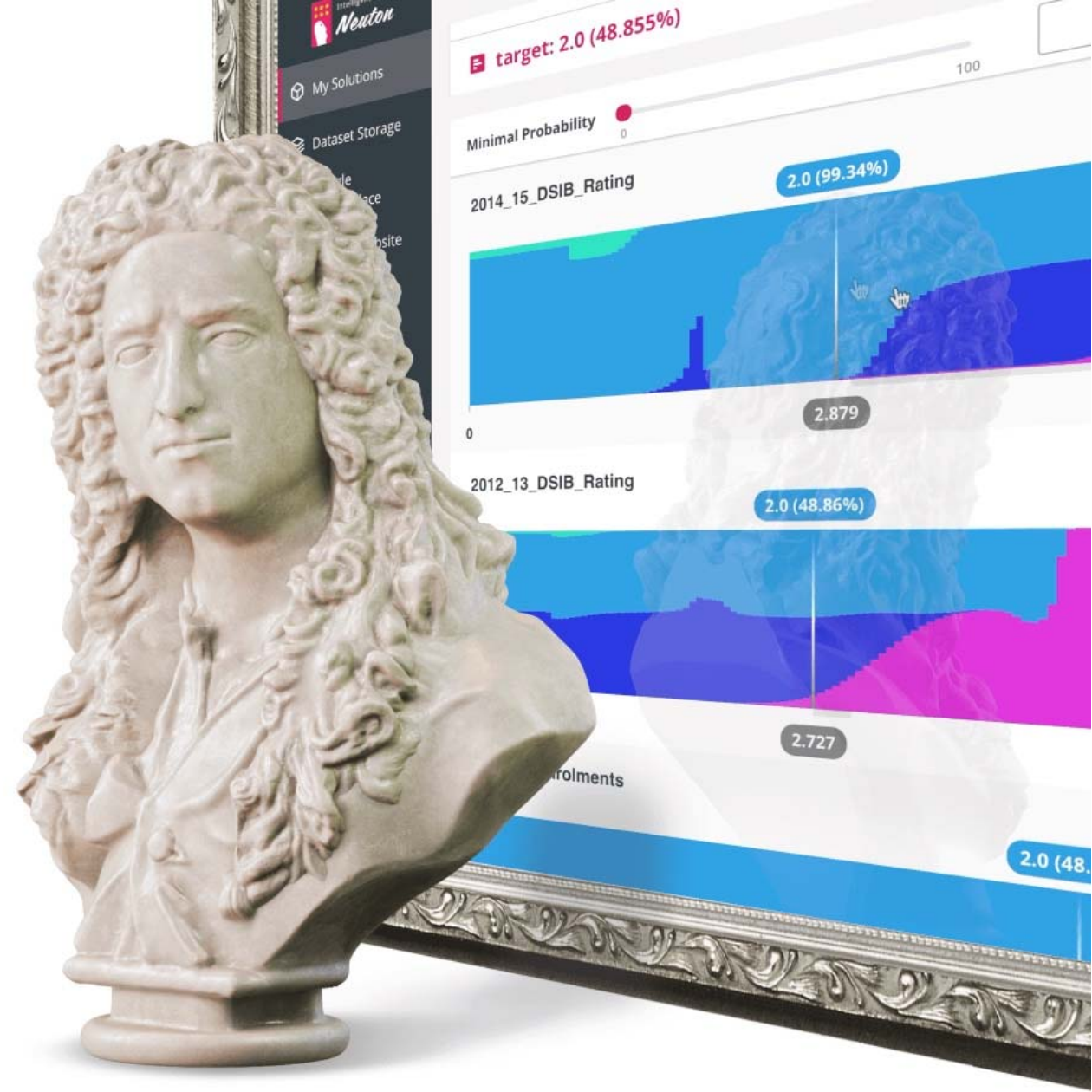
Automated TinyML

Zero-code SaaS solution

**Create tiny models, ready for embedding,
in just a few clicks!**

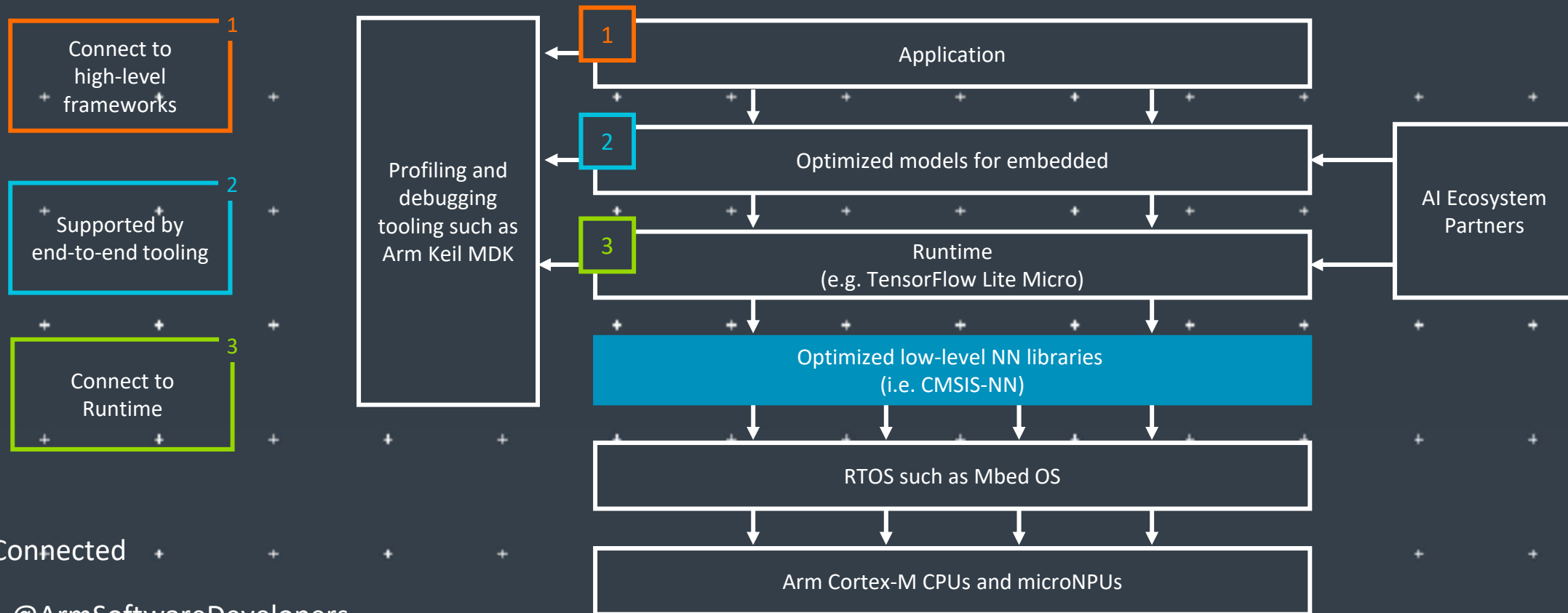
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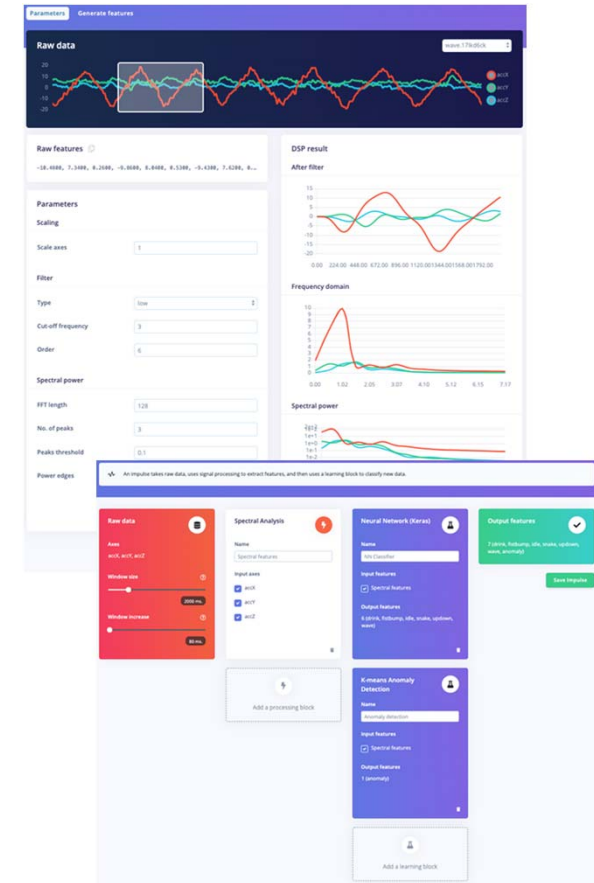
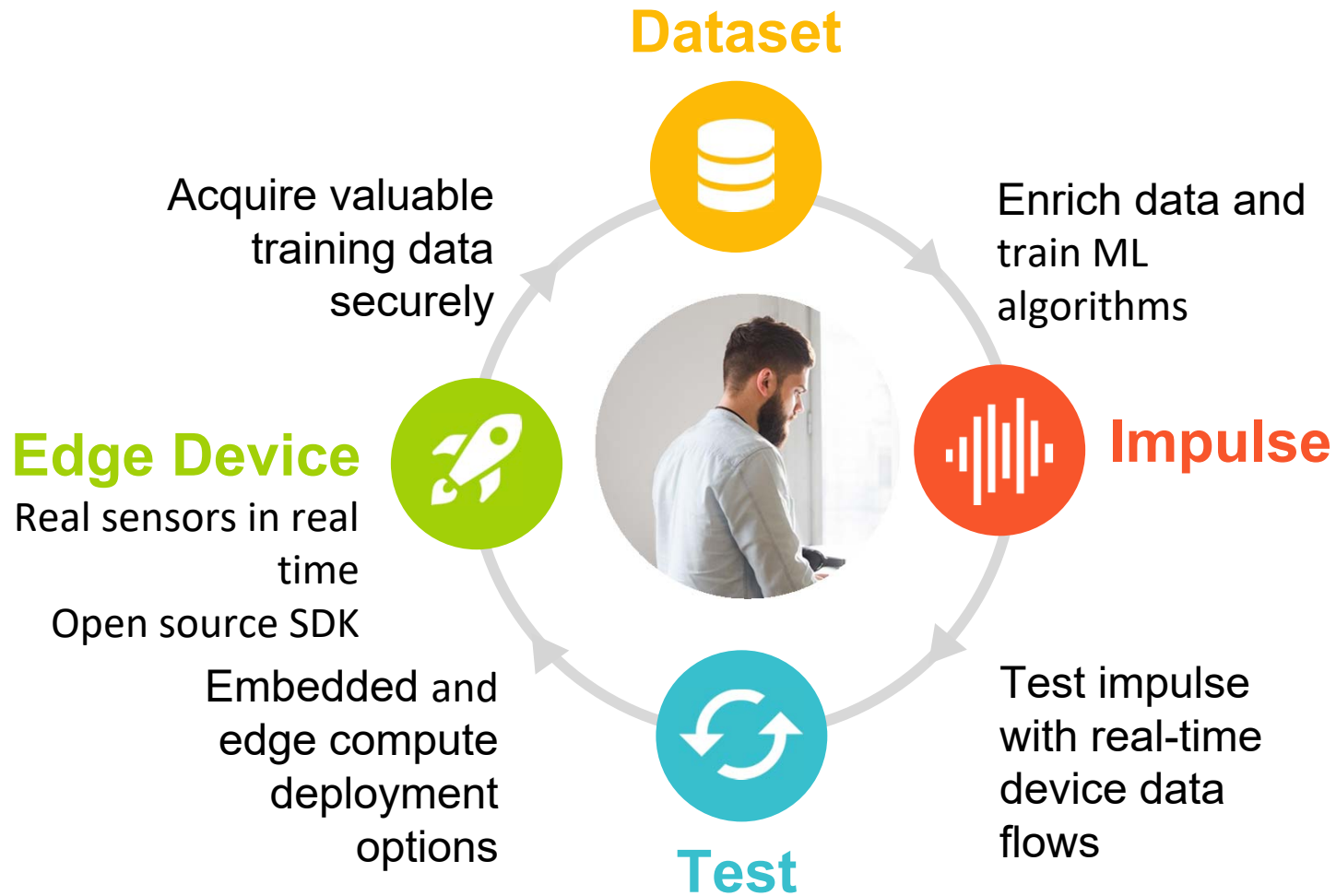
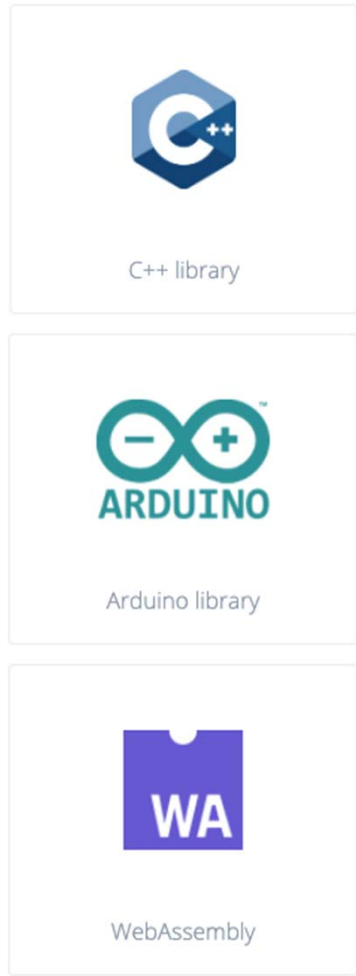
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Resources: developer.arm.com/solutions/machine-learning-on-arm

TinyML for all developers



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Cloud



IoT/IIoT



Automotive



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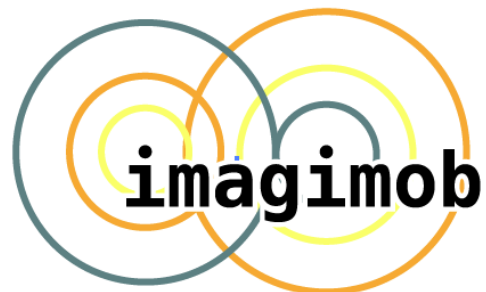
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