tinyML® Trailblazers
Ultra-low power machine learning at the edge success stories

tinyML Success Stories
with Kurt Busch (CEO of Syntiant Corp.)

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Chris Rowen

Chris is a Silicon Valley entrepreneur and technologist, now VP of AI Engineering and Product for Webex Collaboration in Cisco. Most recently he was co-founder and CEO of BabbleLabs, a speech ML company, acquired by Cisco in 2020. Prior to BabbleLabs, Chris served as CTO for Cadence’s IP Group, which he joined after Cadence’s acquisition of Tensilica, the company he founded in 1997 to develop extensible processors. He led Tensilica as CEO and later, CTO, to develop one of the most prolific embedded processor architectures, especially for compute-intensive embedded processing. Chris was a pioneer in developing RISC architecture and helped found MIPS Computer Systems in 1984. He has an MS and PhD in EE from Stanford and a BA in physics from Harvard. He was named an IEEE Fellow in 2015 for his work in development of microprocessor technology.
Kurt Busch is the chief executive officer of Syntiant Corp. He has extensive experience in product development, having driven the successful launch of new products, ranging from SaaS and semiconductors for telecom and broadcast video to consumer electronics and data center systems. Prior to Syntiant, he was president, chief executive officer and a member of the board of directors at Lantronix (NASDAQ: LTRX), a global provider of secure data access and management solutions for Internet of Things (IoT) and information technology (IT). He also served as a senior vice president and general manager of high performance analog business unit at Mindspeed Technologies, acquired by MACOM. Mr. Busch is an engineering hall of fame inductee of the University of California at Irvine, where he earned Bachelor of Science degrees in electrical engineering and biological science. He holds a Master of Business Administration from Santa Clara University.
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1. Connect to high-level frameworks
2. Supported by end-to-end tooling
3. Connect to Runtime

1. Application
2. Optimized models for embedded
3. Runtime (e.g. TensorFlow Lite Micro)

- Optimized low-level NN libraries (i.e. CMSIS-NN)
- RTOS such as Mbed OS
- Arm Cortex-M CPUs and microNPUs

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Resources: developer.arm.com/solutions/machine-learning-on-arm

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Qualcomm AI Research is an initiative of Qualcomm Technologies, Inc.
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